

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re DIVISIONAL application of)
Koji ONO et al)
Based on Serial No. 09/714,891) Group Art Unit: 2815
Filed: November 17, 2000) Examiner: J. Jackson, Jr.
For: SEMICONDUCTOR DEVICE AND)
MANUFACTURING METHOD THEREOF)

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing references known to Applicant and requests that these references be made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a).

The references listed on the attached Form PTO-1449 were cited in parent application Serial No. 09/714,891, and copies of the references can be found in that application.

Respectfully submitted,



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Substitute for form 1449A/PTO INFORMATION DISCLOSURE STATEMENT BY APPLICANT <i>(use as many sheets as necessary)</i>				Complete if Known			
				Application Number		Based on 09/714,891	
				Filing Date		November 17, 2000	
				First Named Inventor		Koji ONO et al.	
				Group Art Unit		2815	
Examiner Name		Jerome Jackson Jr.					
Sheet	1	of	3	Attorney Docket Number		0756-7186	

U.S. PATENT DOCUMENTS						
Examiner Initials [*]	Cite No. ¹	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code ² <i>(if known)</i>			
		5,643,826		Ohtani et al.	07/01/97	
		5,923,962		Ohtani et al.	07/13/99	
		5,247,190		Friend et al.	09/21/93	
		5,399,502		Friend et al.	03/21/95	

FOREIGN PATENT DOCUMENTS								
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		Office ³	Number ⁴	Kind Code ² (if known)				
		JP	07-130652			05/19/95		Abst.
		JP	08-078329			03/22/96		Abst.
		JP	10-135468			05/22/98		Abst.
		JP	10-135469			05/22/98		Abst.
		JP	10-294280			11/04/98		Abst.
		JP	11-191628			07/13/99		Abst.
		JP	10-092576			04/10/98		Abst.
		JP	11-345767			12/14/99		Abst.
		JP	11-354442			12/24/99		Abst.
		pct	WO90/13148			11/01/90		Eng.

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Shimokawa et al., "Characterization of High-Efficiency Cast-Si Solar Cell Wafers by MBIC Measurement," Japanese Journal of Applied Physics, Vol. 27, No. 5, May, pp. 751-758, 1988.	
		Hatano et al., "A Novel Self-Aligned Gate-Overlapped LDD Poly-Si TFT With High Reliability and Performance," IEDM Technical Digest 97, pp. 523-526.	
		Hermann Schenk et al., "Polymers for Light Emitting Diodes," EuroDisplay '99 Proceedings, pp. 33-37.	

Examiner Signature	Date Considered
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹ Unique citation designation number. ² See attached Kinds of U.S. Patent Documents. ³ Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴ For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵ Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. ⁶ Applicant is to place a check mark here if English language Translation is attached.

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		4,394,182		Maddox III	07/19/83	
		5,528,397		Zavracky et al.	06/18/96	
		5,583,369		Yamazaki et al.	12/10/96	
		5,686,328		Zhang et al.	11/11/97	
		5,705,424		Zavracky et al.	01/06/98	
		5,736,750		Yamazaki et al.	04/07/98	
		5,830,787		Kim	11/03/98	
		5,949,107		Zhang	09/07/99	
		6,001,714		Nakajima et al.	12/14/99	
		6,030,667		Nakagawa et al.	02/29/00	

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		Office ³	Number ⁴	Kind Code ⁵ (if known)				
		JP	09-191111			07/22/97		Abst.
		JP	09-293600			11/11/97		Abst.
		JP	10-104659			04/24/98		Abst.
		JP	10-233511			09/02/98		Abst.
		JP	04-369271			12/22/92		Full

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials ¹	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Specifications and Drawings for Application Serial No. 09/619,732, "Semiconductor Device and Manufacturing Method Thereof," Filed on July 19, 2000.	
		Specifications and Drawings for Application Serial No. 09/618,930, "Semiconductor Device and Manufacturing Method Thereof," Filed on July 18, 2000.	
		Specifications and Drawings for Application Serial No. 09/433,705, "Method of Fabricating a Semiconductor Device," Filed on November 4, 1999.	

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		5,323,042		Matsumoto	06/21/94	
		6,198,133	B1	Yamazaki et al.	03/06/01	
		6,281,552	B1	Kawasaki et al.	08/28/01	
		6,166,414		Miyazaki et al.	12/26/00	
		5,393,682		Liu	02/28/95	
		5,100,820		Tsubone	03/31/92	
		6,534,826		Yamazaki	03/18/03	

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		JP	05-102483			04/23/93	Full
		JP	8-274336			10/18/96	Full
		JP	7-235680			09/05/95	Full
		JP	6-148685			05/27/94	Full
		EP	1 003 223		Yamazaki	05/24/00	Eng.
		EP	1 049 167		Yamazaki	11/02/00	Eng.

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